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## MAIL STOP AMENDMENT

Commissioner for Patents

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VERIFICATION OF TRANSLATION OF PRIORITY DOCUMENT

Dear Sir:

The undersigned hereby certifies that I am conversant in both Japanese and English languages, that I have prepared the attached English translation of Japanese Patent Application No. 2002-086196, filed March 26, 2002, and that the English translation is a true, faithful and accurate translation of Japanese Patent Application No. 2002-086196.

I further declare that all statements made of my own knowledge are true and that all statements made on information and belief are with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 USC § 1001, and that such false statements may jeopardize the validity of the application or any patent issuing therefrom.

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## JAPAN PATENT OFFICE

This is to certify that the annexed is a true copy of the following application as filed with this Office.

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	Abstract 1

【Title of Document】 Specification

【Title of Invention】 ON-WAFER MONITORING SYSTEM

【Scope of Claims】

【Claim1】 An on-wafer monitoring system capable of measuring an operation of a plasma treatment apparatus on a wafer, said system comprising:

one or a plurality of sensor sections, a power source unit, and an I/O unit that inputs/outputs signals from/to outside, which are provided on a silicon substrate,

wherein said sensor sections have a plurality of electrodes for separating pattern portions from ions and electrons of plasma by energy, and include electrodes having the same potential as that of said silicon substrate directly under said pattern portions.

【Claim2】 The on-wafer monitoring system according to Claim 1,

wherein a plurality of electrode of said sensor sections are Al electrodes, and space between each of the Al electrodes is insulated by  $\gamma$ -Al<sub>2</sub>O<sub>3</sub>.

【Claim3】 The on-wafer monitoring system according to Claim 2,

wherein the side surface of said Al electrodes is covered with a thin oxide film.

【Claim4】 The on-wafer monitoring system according to any one of Claims 1 to 3,

wherein said power source unit takes out power from plasma potential or takes out power from photoelectromotive force of a PLZT element .

【Claim5】 The on-wafer monitoring system according to any one of Claims 1 to 4,

wherein said I/O unit inputs/outputs signals from/to outside by light.

【Claim6】 The on-wafer monitoring system according to any one of Claims 1 to 5,

wherein said system includes an ion energy analyzer, which has a collector electrode at a sensor section bottom and measures ion current in the collector electrode to obtain ion energy distribution, as said sensor.

【Claim7】 The on-wafer monitoring system according to any one of Claims 1 to 6,

wherein said system includes a photon detector, which detects light made incident into a pattern by photoinduced current generated in an insulating film, as said sensor.

【Claim8】 The on-wafer monitoring system according to any one of Claims 1 to 7,

wherein said photon detector forms a metal thin film on said insulating film, and detects light having energy equivalent to or more than an energy difference between the work function of the metal and the conduction band bottom of said insulating film out of light transmitted the metal

thin film.

**【Claim9】** The on-wafer monitoring system according to any one of Claims 1 to 6, wherein said system includes a photon detector that detects light by a photo diode, as said sensor.

**【Claim10】** The on-wafer monitoring system according to any one of Claims 1 to 9, wherein said system includes an ion radical analyzer, which identifies radicals and ions by detecting light emission by the collision between electrons from an electron gun and radicals or ions, as said sensor.

**【Claim11】** The on-wafer monitoring system according to Claim 10, wherein said ion radical analyzer has a spectroscope for detecting light emission.

**【Claim12】** The on-wafer monitoring system according to any one of Claims 1 to 11, wherein said system includes a probe, which detects at least one of electron current, electron energy distribution, ion current, electron temperature, electron density, and charge storage amount, as said sensor.

**【Detailed description of the invention】**

**【0001】**

**【Technical field to which the invention pertains】**

The present invention relates to a micro nano-sensing system technology for measuring plasma on a wafer surface in a plasma treatment device.

**【0002】**

**【Background Art】**

Fig. 1 shows an outline of a plasma treatment device 100 used in semiconductor manufacturing process. The drawing shows the state where a substrate 130 masked by resist 132 is processed by using plasma 110 in the plasma treatment device 100. Regarding the plasma 110, gas (such as  $\text{Cl}_2$ ) is introduced in vacuum (1 Torr to  $10^{-4}$  Torr), and high-frequency electric field (400kHz to 2.45MHz) is applied to generate the plasma 110. By using the plasma 110 and applying substrate power source (400kHz to 13.56GHz) to the substrate 130 masked by the resist 132, the processing is performed. In the plasma treatment device, vapor phase reaction control of plasma (distribution control of electron energy, optimizing gas molecule structure, dwell time control, and the like) and surface reaction control of the substrate (substrate temperature control, surface impurity control, reactive product control, substrate bias control, and the like) need to be performed.

### 【0003】

As an evaluation method of the plasma treatment device, there is a typical evaluation method using a probe such as Langmuir probe. By the evaluation method using the probe, only the characteristic in the plasma is measured, and the density and the energy of various kinds of particles that are made incident to a material to be processed by the plasma cannot be measured.

In pattern dimensions of a substrate of a material to be processed, which is processed or deposited by the plasma treatment device, microfabrication has been progressed to the order of a few hundred nm. Therefore, needs has been increased for measuring the characteristics of various kinds of particles made incident inside such fine patterns from the plasma.

To measure the characteristics of the particles made incident to the substrate to be processed, pinholes are formed on a flat plate, an energy analyzer of a blocking electric field type is installed behind them, and the incident particles are measured. In the conventional method, the size of the pinholes is about a few mm that is in an entirely different value from actual processing dimensions, and thus it was impossible to accurately measure the incident particles. Further, a differential exhaust system has been required to restrict the changes of energy and charge state caused by the collision among particles in an analyzer tube of an energy analyzer, and the actual measurement on an in-service plasma treatment device was virtually impossible.

As described, a conventional plasma measurement system has a large and complicate apparatus (probe, mass spectrometer, and the like) and causes large disturbance in plasma. For this reason, the most important plasma measurement on a wafer surface where etching reaction progresses in micro nano-scale cannot be performed by the conventional plasma measurement system. In addition, since the cost of apparatus was expensive and so on, it has been difficult to apply it to an actual production apparatus.

Meanwhile, plasma etching of a silicon oxide film is cited as a typical example of the microfabrication processing in the semiconductor manufacturing process. Ions having the energy of a few hundred eV are needed in order to contribute to the etching of the silicon oxide film. In the case of measuring the ion energy distribution by the blocking electric field type energy analyzer, high voltage needs to be applied to a blocking electric field generating electrode.

### 【0004】

#### 【Problem to be solved by the invention】

The object of the present invention is to provide an on-wafer monitoring system capable of

monitoring at the position of a substrate, which is an object to be processed, in the plasma treatment device.

**【0005】**

**【Means for solving problem】**

To achieve the above-described object, the present invention is an on-wafer monitoring system capable of measuring the operation of the plasma treatment device on wafer, which comprises one or a plurality of sensor sections, a power source unit, and an I/O unit that inputs/outputs signals from/to outside, which are provided on a silicon substrate, in which the sensor sections have a plurality of electrodes for separating pattern portions from ions and electrons of plasma by energy, and electrodes having the same potential as that of the silicon substrate directly under the pattern portions.

A plurality of electrode of the sensor section are Al electrodes, and space between each of the Al electrodes may be insulated by  $\gamma$ -Al<sub>2</sub>O<sub>3</sub>. The side surface of the Al electrodes may be covered with a thin oxide film.

The power source unit can be constituted such that power is taken out from plasma potential or power is taken out from photoelectromotive force of a PLZT element. Further, the I/O unit can input/output signals from/to outside via light.

As the sensor, an ion energy analyzer, which has a collector electrode at the bottom of the sensor section and obtains ion energy distribution by measuring ion electric current in the collector electrode, can be included.

As the sensor, it is a photon detector that detects light made incident into the patterns by photoinduced current generated in an insulating film. A metal thin film is formed on the insulating film, and the photon detector can also detect light having energy equivalent to or more than an energy difference between the work function of the metal and the conduction band bottom of the insulating film out of light transmitted the metal thin film.

Furthermore, as the sensor, a photon detector that detects light by a photodiode can be included.

As the sensor, an ion radical analyzer, which identifies radicals and ions by detecting light emission caused by the collision between electrons from an electron gun and radicals or ions, can be included, and the ion radical analyzer can have spectroscopy for detecting light emission.

As the sensor, a probe that detects at least one of electron current, electron energy



distribution, ion current, electron temperature, electron density and charge storage amount can be included.

#### **【0006】**

##### **【Mode for carrying out the claimed invention】**

The embodiments of the present invention will be described in detail with reference to the drawings.

Fig. 2 and Fig. 3 show the basic structure of the on-wafer monitoring system according to the present invention.

Fig. 2 shows the on-wafer monitoring system 200 where sensors and the like are created on a substrate and placed on the position of a substrate to be treated in the plasma treatment device 100. The on-wafer monitoring system 200, as shown in Fig. 3, comprises various kinds of sensors which are created on the substrate, a data I/O unit 210 that inputs/outputs data from/to outside via light, and a power source unit 250 that supplies power source to them. The on-wafer data I/O unit 210 is connected to a laser diode (LD) 320 and a photo diode (PD) 330, which are I/O units via light installed outside, and it receives instructions from outside and transmits monitored data to the outside.

The on-wafer monitoring system 200 shown in Fig. 2 and Fig. 3 is an example where an ion energy analyzer 400, a VUV photon detector 500 and a radical ion species emission spectrophotometer 600 as sensors are provided on the substrate. The constitution of the sensors will be described in detail later.

#### **【0007】**

As shown in Fig. 3, the measurement systems are integrated on an Si wafer, and measurement in various kinds of plasma process devices can be easily performed. Fig. 3 shows an example where a voltage driving system and a signal detection system were built by forming an electric circuit on the Si wafer, but a photo-detecting element and a light-emitting element are installed on the Si wafer and driving of the sensors and detection of signals can be also controlled from outside a vacuum equipment by using light. The feature of the present invention is to measure plasma process wirelessly by using micro sensors. Basically, the sensor section and the signal processing section of the on-wafer monitoring system are developed using micromachine technology and LSI technology, respectively.

The size of one chip is approximately 2cm × 2cm, for example. A wafer size is 8 inches

and 12 inches, ultimately. Further, the system is not a TEG (test element group) style, but is fabricated on an exclusive wafer and shall be capable of measuring all the distribution within a wafer plane.

As described, the ion energy analyzer, the probe based on the ion energy analyzer, the photon detector, and the ion species and radical species emission spectrophotometer are integrated with a lock-in amplifier, the photo diode, a memory circuit, a switching circuit, a filter circuit, and an LSI of I/O signal system, and a power source system is also fabricated into one chip. All data transmission/receiving is performed via optical interconnect and a complete wireless in-line system can be built.

Furthermore, as shown in Fig. 2, by transmitting acquired data wirelessly to a computer 310 using the on-wafer monitoring system, the process of plasma treatment can be simulated in real-time. In addition, prediction by the simulation is fed back to a plasma generator, and the process can be controlled during process.

#### 【0008】

<I/O unit>

Optical interconnect technology by the photo diode (PD) is used for wireless transmission/receiving of signals.

Fig. 4 shows a constitution example of the I/O unit. Fig. 4(a) shows a receiving section by photo diodes 212, 214. The photo diode 212 receives the instruction via optical signals from an outside photo diode (LD), and a difference is taken by the output from the photo diode 214 receiving background light and an operation amplifier 216. Then, a selective circuit 218 sends it to each sensor as a instruction.

As described, wireless communication between the outside and the on-wafer monitoring system 200 can be performed by optical signals.

#### 【0009】

<Power source>

Incidentally, the power source 250 that supplies power to each kind of sensor is created on the substrate, and controls voltage by connecting to a voltage variable circuit utilizing potential (a few tens V to a few hundred V) generated by plasma in the plasma treatment device 100.

Fig. 5 shows a construction example of the power source unit 250. In Fig. 5, electrodes 271 to 273 receive a potential difference between plasma space potential and ground in the plasma

110. On receiving the potential difference, a decoder controls current that flows in transistors 252 to 254 and transistors 255 to 257, and thus obtaining desired voltage  $V=nRI$ . Note that  $R$  and  $n$  denote the resistance value of resistors 262 to 264 and the number of resistors, respectively.

When higher voltage is required, a PLZT (Lead Lanthanum Zirconium Titanate) element is used. The element is capable of supplying voltage of approximately 3kV/cm. Its example is shown in Fig. 6. In Fig. 6, when ultraviolet ray having a wavelength of 365nm or higher is irradiated to the PLZT (Lead Lanthanum Zirconium Titanate) element 280 to which polarization treatment was performed, internal current corresponding to the irradiation light intensity flows, and the voltage of 3.3kV/cm occurs between an electrode 282 and an electrode 284 depending on a distance between electrodes. As the ultraviolet ray to be used, ultraviolet ray from plasma or ultraviolet ray irradiated from outside is used. Consequently, a high-voltage power source can be installed inside the on-wafer monitoring system 200.

As described, the PLZT (Lead Lanthanum Zirconium Titanate) device, which is capable of taking out the potential generated on the wafer as shown in Fig. 5 or capable of generating high voltage by ultraviolet ray irradiation as shown in Fig. 6, can be used in the power source supplied to the on-wafer monitoring system 200.

#### 【0010】

##### <On-wafer ion energy analyzer>

Firstly, as an example of sensors mounted on the on-wafer monitoring system 200, description will be made for the constitution of the ion energy analyzer by using Fig. 7 and Fig. 8. Fig. 7(a) is a plan view seen from above, and Fig. 7(b) is a sectional view of Fig. 7(a) cut at A-A'. An area having a pattern size corresponding to the semiconductor device ( $\text{SiO}_2$  421 in the drawing) and a collector electrode 413 in a layer under an analyzing device area of Fig. 7(b) make a structure where they are formed in a zigzag linear state as shown in Fig. 7(a). Fig. 8 shows the constitution of the entire measurement system using the electrodes and the like shown in Fig. 7.

If predetermined pores (100nm, for example) are only formed in the analyzing device area in order to include the pattern area ( $\text{SiO}_2$  421) corresponding to the semiconductor device and the analyzing element area individually, a pore diameter and a depth of the analyzing element can be freely designed to the order of a few  $\mu\text{m}$ . The disturbance to a surface sheath and plasma can be prevented by making the pattern size smaller than a device length.

The ion energy analyzer is required to be capable of measuring voltage and current

characteristics in a high-aspect pattern. Therefore, as shown in Fig. 7, an electrode to which voltage is applied is embedded under the pattern of the SiO<sub>2</sub> layer 421 that is a plasma treatment object. Since voltage to be applied is as extremely high as up to 1kV on the assumption of silicon oxide film etching, an insulating film material requires high withstanding voltage. Thus, selection of material is quite important. In the present circumstances, Al<sub>2</sub>O<sub>3</sub> that is anodized aluminum should be used.

#### 【0011】

An electrode for electron-retarding voltage application 424, an electrode for ion energy separation voltage 426, and an electrode for removing secondary electrons 428 are arranged on a sidewall electrode. As shown in Fig. 8, blocking voltage to inflow electrons V<sub>1</sub> 462, voltage for separating ion energy V<sub>2</sub> 464, and voltage for removing secondary electrons V<sub>3</sub> 466 are applied to each electrode. Further, the uppermost (directly under the SiO<sub>2</sub> layer 421) sidewall electrode 422 is directly connected to an Si layer 411 into a same potential as shown in Fig. 8. This is because the Si layer 411 is under the SiO<sub>2</sub> layer 421 in a regular plasma etching treatment, and the connection is made in order to realize it in this sensor in a simulated manner.

By properly setting the voltage of each electrode, electron current/energy and ion current/energy, which are made incident to the etching pattern (SiO<sub>2</sub> layer 421 in Fig. 7) can be measured, and in addition, charged particles such as electrons and ions are removed and neutral particles and photons can be taken out from the incident particles.

#### 【0012】

In the ion energy analyzer or the like, high voltage of a few hundred V or higher must be applied to micro regions to separate incident ion energy, and Al<sub>2</sub>O<sub>3</sub> having high withstanding voltage is used for the insulating film. An aluminum electrode surface to which voltage is applied is covered with a thin oxide film, and has a structure that is not affected by deposit of polymers or the like.

In the ion energy analyzer 400, as shown in Fig. 8, synchronized voltage of linearly increasing voltage and voltage having constant frequency from a frequency generator 452 is applied as the voltage for separating ion energy V<sub>2</sub> 464. Then, to measure micro ion current flowing in a collector electrode 413, noise is removed using a lock-in amplifier 454 to measure voltage and ion current characteristic regarding the noise from plasma. The value is differentiated to calculate the ion energy distribution.

### 【0013】

Each electrode is separated by the insulating material, and  $\gamma$ - $\text{Al}_2\text{O}_3$  having high withstanding voltage used as a barrier film of an electrolytic capacitor is used as the insulating material between the electrodes in the present invention. Another insulating material (such as  $\text{SiO}_2$  and another ceramic material) may be used according to the energy of charged particles to be measured. Further, the number of electrodes for generating blocking electric field may be decreased depending on a measurement type and required measurement accuracy.

There are cases where deposit is formed on the structure during actual plasma process, and the fluctuation of the blocking electric field caused by the deposit is a problem. For example, when performing etching to  $\text{SiO}_2$ , an organic material having a structure similar to Teflon could deposit on the sidewall electrode when the etching is performed by using C-F series gas. In the present invention, a film having sufficient capacity ( $\text{Al}_2\text{O}_3$ , for example) is formed on the electrode surface exposed on the sidewall to restrict affects of the deposit to measurement.

### 【0014】

The disturbance to the surface sheath and plasma can be restricted almost completely by making the pattern size smaller than the device length and making the potential of a sensor surface the same as the substrate.

The lock-in amplifier is used for noise, and a method where the noise is removed and the ion current is measured is used. Although it is possible to form a lock-in amplifier circuit in the substrate as described above, the optical interconnect is used to transmit the signals to the outside and the lock-in amplifier may be installed outside.

The above-described on-wafer ion energy analyzer 400 is capable of measuring the distribution of the ion energy made incident to the pore structure, which is formed in the patterns of the  $\text{SiO}_2$  layer, without being affected by the noise from plasma and deposited film.

### 【0015】

The structure of the ion energy analyzer is formed on the Si wafer by applying semiconductor device manufacturing technology. Fig. 9 and Fig. 10 are one example of the manufacturing process.

In Fig. 9(a) (1), after oxidizing the silicon substrate 411 to form the oxide film 412 on the substrate and depositing aluminum, patterning is performed in the zigzag linear state, for example, as shown in Fig. 7(a) to form an aluminum electrode (collector electrode 413). Next, anodization

is performed to form a  $\gamma$ - $\text{Al}_2\text{O}_3$  layer on the aluminum layer and RIE (reactive ion etching) is performed to the  $\text{Al}_2\text{O}_3$  layer formed, and thus pores are formed (refer to Fig. 9(a)(2)).

As shown in Figs. 9(b)(1) to (3), the  $\text{SiO}_2$  layer is formed by CVD (chemical vapor deposition) on the Al layer 413 of the collector electrode for protection purpose, CMP (chemical mechanical polishing) is conducted to the formed  $\text{SiO}_2$  layer to make it flat with the  $\gamma$ - $\text{Al}_2\text{O}_3$  layer. Aluminum is sputtered on it to form an electrode. Then, after performing RIE to the electrode to form the pores, anodization is conducted to form a  $\gamma$ - $\text{Al}_2\text{O}_3$  layer 427. In conducting the anodization, the  $\text{Al}_2\text{O}_3$  layer is formed on areas that are exposed to the side surface of the aluminum electrode.

Subsequently, the sputtering of the aluminum electrode on the  $\gamma$ - $\text{Al}_2\text{O}_3$  layer, the formation of pores by RIE, and the formation of the  $\gamma$ - $\text{Al}_2\text{O}_3$  layer by anodization are repeated for plural times (twice in the drawing), and the sputtering of the aluminum electrode and the formation of pores by RIE are finally performed, which is shown in Fig. 9(c).

Next, as shown in Fig. 10(d)(1), the  $\text{SiO}_2$  layer for protection that has been previously formed by CVD, is removed by RIE, and then, polycrystalline silicon is formed including the inside of pores. Then, the polycrystalline silicon is etched back for planarization (refer to Fig. 10 (d)(2)). Then, as shown in Fig. 10 (d), an  $\text{SiO}_2$  layer is formed on a planarized plane and pores are formed on the formed  $\text{SiO}_2$  layer by RIE. Eventually, the polycrystalline silicon is removed by RIE to allow the pore pass completely through the collector electrode 413 (refer to Fig. 10(f)).

As described, the ion energy analyzer can be formed on the Si wafer by applying the semiconductor device manufacturing technology.

#### 【0016】

##### <On-wafer photon detector>

The on-wafer photon detector detects the intensity and the wavelength of light such as vacuum ultraviolet ray (VUV), which is made incident into the patterns, by converting them into the photoinduced current generated in the insulating film such as a silicon oxide film, a silicon nitride film and a silicon oxynitride film. It can detect light having energy exceeding the energy band gap of each material. For example, the silicon oxide film has 8.8eV by which vacuum ultraviolet ray can be measured. The silicon nitride film has 5eV and the silicon oxynitride film has a value between the previous values.

Furthermore, when a metal thin film of approximately 1 $\mu\text{m}$  is placed on the insulating film,

it transmits only light exceeding plasma frequency determined by the electron density in the metal. Further, by making the metal film extremely thin, it also transmits wavelength same as plasma frequency or less. At this point, light corresponding to an energy difference between the work function of metal and the conduction band bottom of the insulating film is absorbed at the interface between the metal film and the insulating film of the metal, and electron carriers are generated in an insulating film conduction band, that is, the photoinduced current is generated. Thus, it is possible to detect light having a particular wavelength. By changing the type and the film thickness of the metal, intrinsic plasma frequency or work function can be changed. By changing the type and film thickness of the insulating film, intrinsic band gap can be changed. Consequently, by combining insulating film materials, metals, and semiconductors (Si), light of various wavelengths can be detected. Furthermore, voltage for retarding electrons and ions is applied to make only emitted light incident into the pattern, and measurement can be done, and electrons and ions can be removed by depositing the metal film on the insulating film as well.

Fig. 11(a) shows the constitution of a photon detector 500. Although its constitution is basically the same as that of the ion energy analyzer 400 shown in Fig. 7, and the surface of an electrode 513 at the bottom is covered by an SiO<sub>2</sub> film 514 as shown in Fig. 11(a). As described, the silicon oxide film 514 may be the insulating film such as the silicon nitride film and the silicon oxynitride film, and it can identify the wavelength of light detected by the type and the thickness of the insulating film. In the insulating film 514, the absorbance to the wavelength of light is known and the film is installed while the film thickness is controlled. The collector electrode 513 collects and measures the photoinduced current generated in the insulating film 514, and thus photons are measured. By monitoring the current flowing in the collector electrode 513, information regarding the wavelength of light that is made incident into the pattern can be obtained.

Furthermore, as shown in Fig. 11(b), a constitution is acceptable where a mirror 563 processed into the angle of 45 degrees is formed on an Si substrate 561 instead of the collector electrode, only incident light is reflected, and a photo diode 550 measures the incident light intensity.

In the photon detector 500 of Figs. 11(a) and (b), the same voltage as in the ion energy analyzer shown in Fig. 8 is applied to each electrode to prevent electrons and ion from reaching the collector electrode and the like. In addition, the forming method is the same as Fig. 9 and Fig. 10.

### <On-wafer radical ion species emission spectrophotometer>

The on-wafer radical ion species emission spectrophotometer is used instead of the mass spectrometer in order to identify ions and radical species which are made incident to the substrate. The mass spectrometer requires a certain amount of distance to separate mass, and it is difficult to fabricate it on the silicon substrate. Further, loss or various reactions could occur because of long-range transportation and measurement of good accuracy is difficult. For this reason, the emission spectrophotometer, by which identification of ion species and radical species in micro regions are realized and which uses a micro electron gun that can cope with microfabrication and integration, has been developed.

The on-wafer radical ion species emission spectrophotometer 600 shown in Fig. 12 separates ions, electrons and radicals, which are made incident to a pattern 644, by using the same electrode structure (638 to 644) as the ones used in the above-described ion energy analyzer. Voltage for blocking electrons 622 and voltage blocking ions 624 are applied to the electrodes. Note that the electron structure used here is constituted by silicon and silicon nitride, but Al and  $\text{Al}_2\text{O}_3$  may be used in the same manner as Fig. 7.

Incident radicals and ions emit light by an energy variable electron beam generated by a micro electron gun 610, and the light emission is propagated to the micro spectroscopy for spectroscopy. An optical waveguide 645 is used for propagating the light emission to the spectroscopy. The spectroscopy can perform spectroscopy with the resolution of 20nm or less.

Further, it is constituted that an ion collector electrode 615 simultaneously measures ionization potential of radicals by the electron beam and identification of radicals or the like can be performed with the light emission spectroscopy. Herein, voltage of a power source 614 is changed, applied voltage to the ion collector electrode 615 is changed to measure voltage-current characteristics, appearance potential intrinsic to neutral particles is calculated, and thus emission of radicals can be detected.

### 【0018】

In the emission spectroscopy detecting ion species and radical species, an electron beam source to allow ions and radicals to emit light is manufactured by using a micro field emission type electron gun. Its description will be made using Fig. 13 and Fig. 14.

Fig. 13 is the field emission type electron gun described in the thesis (JJAP 36 (1997) L939). In the electron gun, an aluminum electrode is provided on an oxide film 652 on a silicon substrate



651. A silicon layer 654 and a silicon oxide film 655 are provided on it and a platinum electrode 656 is created. Further, an electrode 657 for applying electric field to emit electrons is provided 1cm above the platinum electrode 656. When the voltage of 110V and the voltage of 5kV are applied between the aluminum electrode 653 and the platinum electrode 656 and between the platinum electrode 656 and the electrode 657 above it, respectively, emitted electron density of  $1.4\text{mA}/\text{cm}^2$  is obtained.

Meanwhile, the expression of Fowler-Nordheim regarding field emission type electron current depends on work function  $\phi[\text{eV}]$  and field intensity  $F[\text{Vcm}^{-1}]$ , and the following holds with respect to current density  $J_0$ .

[Expression 1]

$$J_0 = \frac{1.54 \times 10^{-6} F^2}{\phi} \exp \left\{ \frac{-6.83 \times 10^7 \phi^{3/2}}{F} \right\} [A \cdot \text{cm}^{-2}]$$

Herein, supposing the gap between electrodes is 1cm and the applied voltage is 5kV, the field intensity is  $5 \times 10^3 [\text{V}/\text{cm}]$ . When it is microfabricated to set the gap between electrodes to 1/1000, 5V should be applied to obtain the same field intensity.

【0019】

This is realized into the electron gun shown in Fig. 14. Fig. 14(a) is a perspective view and Fig. 14(b) is a plan view seen from above. With the same construction as Fig. 13, an aluminum electrode 632, a silicon layer 633, and  $\text{SiO}_2$  layer 634, a platinum electrode 635 and a platinum electrode 636 constitute the electron gun. A cover 616 for the electrode 615 and a cover for avoiding deposition 637 are formed by  $\text{Si}_3\text{N}_4$ , which prevents deposit from attaching to the electrode 615 and the platinum electrode 635.

Light emission by the electron gun is propagated by the waveguide 645, where an SiON film is sandwiched by  $\text{SiO}_2$  films or Mg is doped on the surface of  $\text{SiO}_2$ , and a micro spectroscopy 650 performs spectroscopic diffraction.

【0020】

Fig. 15 describes the construction of the micro spectroscopy 650. As shown in Fig. 15, input light is reflected by a blazed grating 690 that is processed into a saw-tooth shape, for example, which is provided on the optical waveguide, the light is spectrally factorized into wavelengths  $\lambda_1$  to  $\lambda_3$ , and photo-detecting element 674 to 678 detects them. As described, the micro spectroscopy 650 is constituted by forming a diffraction element by a semiconductor element. The optical

waveguide is formed in such a manner that an ( $n_1$ ) layer having high refractive index in an ( $n_2$ ) layer having low refractive index as shown in (a), or the ( $n_1$ ) layer having high refractive index is sandwiched by the ( $n_2$ ) layers having low refractive index as shown in (b). To form the layers having different refractive index, the layer having low refractive index is fabricated in the  $\text{SiO}_2$  layer by iondoping or the waveguide may be formed by polyimide.

By using the micro spectroscopy, even micro light is detected at high sensitivity. In the case of radicals, appearance potential is monitored by observing current values ionized simultaneously. The individual micro sensor technology is integrated.

#### 【0021】

An example of the manufacturing process of the on-wafer radical ion species emission spectrophotometer 600 will be described using Fig. 16 and Fig. 17.

In Fig. 16(a), an oxide film 631 is formed on a silicon substrate 630. Silicon is deposited on the oxide film 631 and etching is performed to form an Si layer 633 (refer to Fig. 16(b)). Vapor deposition of aluminum is performed to the Si layer 633 to form the electrode 632 (refer to Fig. 16(c)). Further,  $\text{SiO}_2$  is deposited on the Si layer 633 and etching is performed to form the  $\text{SiO}_2$  layer 634 (refer to Fig. 16(d)).

Next, as shown in Fig. 16(e), platinum is deposited and patterning is performed to form the electrode 635 and the electrode 636. Then, by depositing  $\text{Si}_3\text{N}_4$  and performing etching, the covers 637, 616 protecting the electrodes are formed (refer to Fig. 16(f)).

As shown in Figs. 17(g), (h),  $\text{SiO}_2$  (including a sacrifice layer) is deposited and etching is performed,  $\text{Si}_3\text{N}_4$  is deposited on the formed  $\text{SiO}_2$  and etching is performed, and thus completing the cover 616 for the electrode 615. Next, to fabricate the waveguide 645,  $\text{SiO}_2$  is deposited and performing etching, ion doping is conducted (refer to Fig. 17(i)).

As shown in Figs. 17(j) and (k), to form an electrode for blocking electrons and ions from plasma, after  $\text{Si}_3\text{N}_4$  is deposited and etching is performed, the sacrifice layer of  $\text{SiO}_2$  that was used in forming the electrode is also removed by etching. Note that, in Figs. 17(j) and (k), the  $\text{SiO}_2$  layer 644 and the electrode Si 643 are also formed, for example, as pattern layers, as shown in FIG.12.

#### 【0022】

Fig. 18 shows another constitution of the electron gun. Although Fig. 14 illustrates a constitution where electrons are emitted from three sides of a square and the waveguide 645 is

installed on one side, its shape may be a circular shape as shown in Fig. 18. In Fig. 18, by applying voltage to an electrode for electron emission 712 and a mesh electrode 714, which are concentric circles, electrons emitted from the electron gun can be focused on a specified region (central area of the concentric circles) 716.

Further, Fig. 19 shows the outline of the ion radical analyzer that is an ion radical analyzer using a constitution, where a light interference multilayer film and a photo diode are combined, is used as an analyzer for observing light emission from the electron gun. In the outline constitution shown in the drawing, light emission of neutral particles or the like by electron collision is detected by a narrow-band transmission optical filter 844 constituted by the light interference multilayer film and a photo diode 843. As an electron gun 810 of the constitution of this ion radical analyzer, it is suitable to use the electron gun constitution shown in Fig. 18. Note that a photo diode 842 constitutes a photon detector detecting light such as ultraviolet ray and the like that transmit the light interference multilayer thin film 844.

【0023】

<On-wafer probe>

The on-wafer probe, as shown in Fig. 20, has basically the same structure as the ion energy analyzer shown in Fig. 7, where a collector as the bottom electrode performs voltage and current measurement. It is a micro probe capable of measuring electron current entered into etching patterns, electron energy distribution, ion current, electron temperature, electron density, charge storage amount, and the like by controlling voltage and the like applied to each electrode. It can be mounted as a sensor of the on-wafer monitoring system.

【0024】

<Other application>

After exposing the sensor structure shown in Fig. 7 and Fig. 20 to plasma, constant voltage is applied between each electrode to measure the current flowing between the electrodes, by which aspect dependence of sidewall conductivity can be easily measured.

Further, after each electrode is connected to the control electrode of a flash memory and exposed to plasma, charge accumulated in a floating electrode is measured as  $V_{th}$  shift of a control electrode, the depth dependence of an electric charge by plasma process can be evaluated at one time.

【0025】

#### <Affects by deposition>

The plasma on-wafer monitoring is primarily on the assumption of plasma etching process. Particularly, it is believed that deposition of polymers or the like during silicon oxide film etching affects monitoring. Consequently, the present invention is designed to restrict the affects of a polymer deposition film at a minimum level. In the ion energy analyzer, since voltage is applied to the sidewall to perform electron retarding and energy separation of ions, the polymer deposition on the sidewall cause affects. Thus, by covering the sidewall electrode surface with a thin insulating film, it is possible to create a state where the capacity of sidewall changes little in spite of the deposition of polymers or the like, and voltage fluctuation or the like can be restricted.

Further, because high-energy ions constantly make an impact to the ion collector electrode area at a contact hole bottom, the deposition of polymers is extremely small, but it is required to implement a sequence that high voltage is applied to the bottom after measurement and the surface is cleaned by Ar ions or oxygen ions to improve reliability and to lengthen the number of repeated use. The affects of the deposited film can be also restricted for the probe and the photon detector by the same method. On the other hand, regarding the ion species and radical species emission spectrophotometer, it is possible for an incident ion lens system to restrict the affects of the deposited film or the like by the same design as the ion energy analyzer.

The micro electron gun, the optical waveguide and the micro spectroscopy are devised to prevent radicals from reaching them by installing a nitride film slit for covering the electron gun or devising layout.

#### 【0026】

<Measures to disturbance of ion sheath by elements, increase of elements temperature, and electromagnetic noise>

The disturbance of the ion sheath by elements can be prevented by bringing the electrode on the substrate surface and the silicon substrate into the same potential. Further, the increase of elements temperature can be sufficiently restricted by substrate cooling of an etching apparatus, because all sensors are fabricated inside the silicon substrate with the size on the order of  $\mu\text{m}$ . The electromagnetic noise can be solved by fabricating the circuit of the lock-in amplifier in the silicon substrate and measuring ion current while the noise is removed.

#### 【0027】

【Effect of the invention】

As stated above, an on-wafer monitoring system according to the invention is capable of measuring the operation of the plasma treatment device on wafer, which is capable of monitoring at the position of a substrate, which is an object to be processed, without any conversion in the plasma treatment device.

**【Brief description of drawing】**

**【Fig.1】** Fig. 1 is a view showing the outline of a plasma treatment device.

**【Fig.2】** Fig. 2 is a view showing the schematic constitution of an on-wafer monitoring system of the present invention.

**【Fig.3】** Fig. 3 is a view showing a constitution example of the on-wafer monitoring system on a wafer.

**【Fig.4】** Fig. 4 is a view showing a constitution of an I/O unit of the on-wafer monitoring system.

**【Fig.5】** Fig. 5 is a view showing a constitution of a power source of the on-wafer monitoring system.

**【Fig.6】** Fig. 6 is a view showing another constitution of the power source.

**【Fig.7】** Fig. 7 is a view showing a constitution of an on-wafer ion energy analyzer.

**【Fig.8】** Fig. 8 is a view showing a measurement system of the ion energy analyzer.

**【Fig.9】** Fig. 9 is a view showing a manufacturing process of the ion energy analyzer.

**【Fig.10】** Fig. 10 is a view showing a continued manufacturing process of the ion energy analyzer.

**【Fig.11】** Fig. 11 is a view showing a constitution of a photon detector.

**【Fig.12】** Fig. 12 is a view showing a constitution of an ion radical analyzer.

**【Fig.13】** Fig. 13 is a view showing a basic constitution of an electron gun.

**【Fig.14】** Fig. 14 is a view showing a constitution of the electron gun used in the ion radical analyzer.

**【Fig.15】** Fig. 15 is a view showing a constitution of a micro spectroscopy.

**【Fig.16】** Fig. 16 is a view showing a manufacturing process of the ion radical analyzer.

**【Fig.17】** Fig. 17 is a view showing a continued manufacturing process of the ion radical analyzer.

**【Fig.18】** Fig. 18 is a view showing another constitution of the electron gun.

**【Fig.19】** Fig. 19 is a view showing another schematic constitution of the ion radical analyzer.

【Fig.20】 Fig. 20 is a view showing the constitution of a micro probe.

Explanation of Reference Numeral

【Explanation of letters or numerals】

- 100: Plasma treatment device
- 110: Plasma
- 130: Silicon substrate
- 132: Resist
- 200: On-wafer monitoring system
- 210: Data I/O unit
- 212, 214: Photo diode
- 216: Operation amplifier
- 218: Selective circuit
- 250: Power source unit
- 252, 253, 254, 255, 256, 257: Transistor
- 262, 263, 264: Resistor
- 271, 272, 274: Electrode
- 280: PLZT element
- 282, 284: Electrode
- 400: Ion energy analyzer
- 411: Silicon substrate
- 412: Oxide film
- 413: Collector electrode
- 421: SiO<sub>2</sub> layer
- 422: Sidewall electrode
- 424: Electrode for electron retarding voltage application
- 426: Electrode for ion energy separation voltage
- 427: Insulating layer
- 428: Secondary electron removing electrode
- 452: Frequency generator
- 454: Lock-in amplifier
- 500: Photon detector

513: Collector electrode  
514: SiO<sub>2</sub> film  
550: Photo diode  
561: Silicon substrate  
563: Mirror  
600: Radical ion species emission spectrophotometer  
610: Micro electron gun  
614: Power source  
615: Ion collector electrode  
616: Cover  
622: Voltage for electron deceleration electrons  
624: Voltage for ion deceleration  
630: Silicon substrate  
631: Oxide film  
632: Aluminum electrode  
633: Silicon layer  
634: SiO<sub>2</sub> layer  
635, 636: Platinum electrode  
637: Cover for avoiding deposition  
644: Pattern  
645: Optical waveguide  
650: Micro spectroscope  
651: Silicon substrate  
652: Oxide film  
663: Aluminum electrode  
654: Silicon layer  
655: Silicon oxide film  
656: Platinum electrode  
657: Electrode  
674: Photo-detecting element  
690: Blazed grating

712: Electrode for electron emission

714: Mesh electrode

810: Electron gun

842, 843: Photo diode

844: Narrow-band transmission optical filter

637, 616: Cover



**【Title of Document】**

**Drawings**

Fig. 1

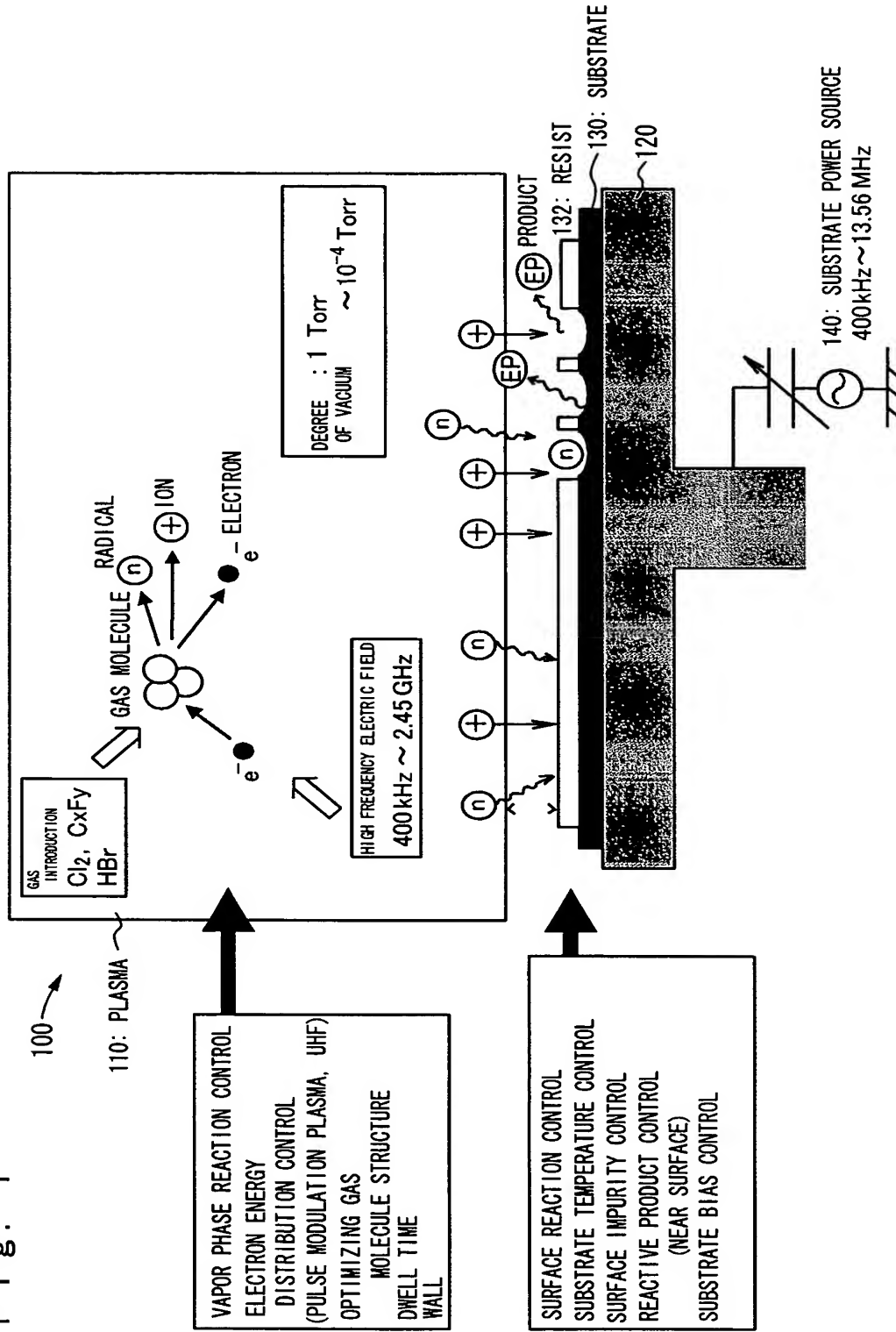
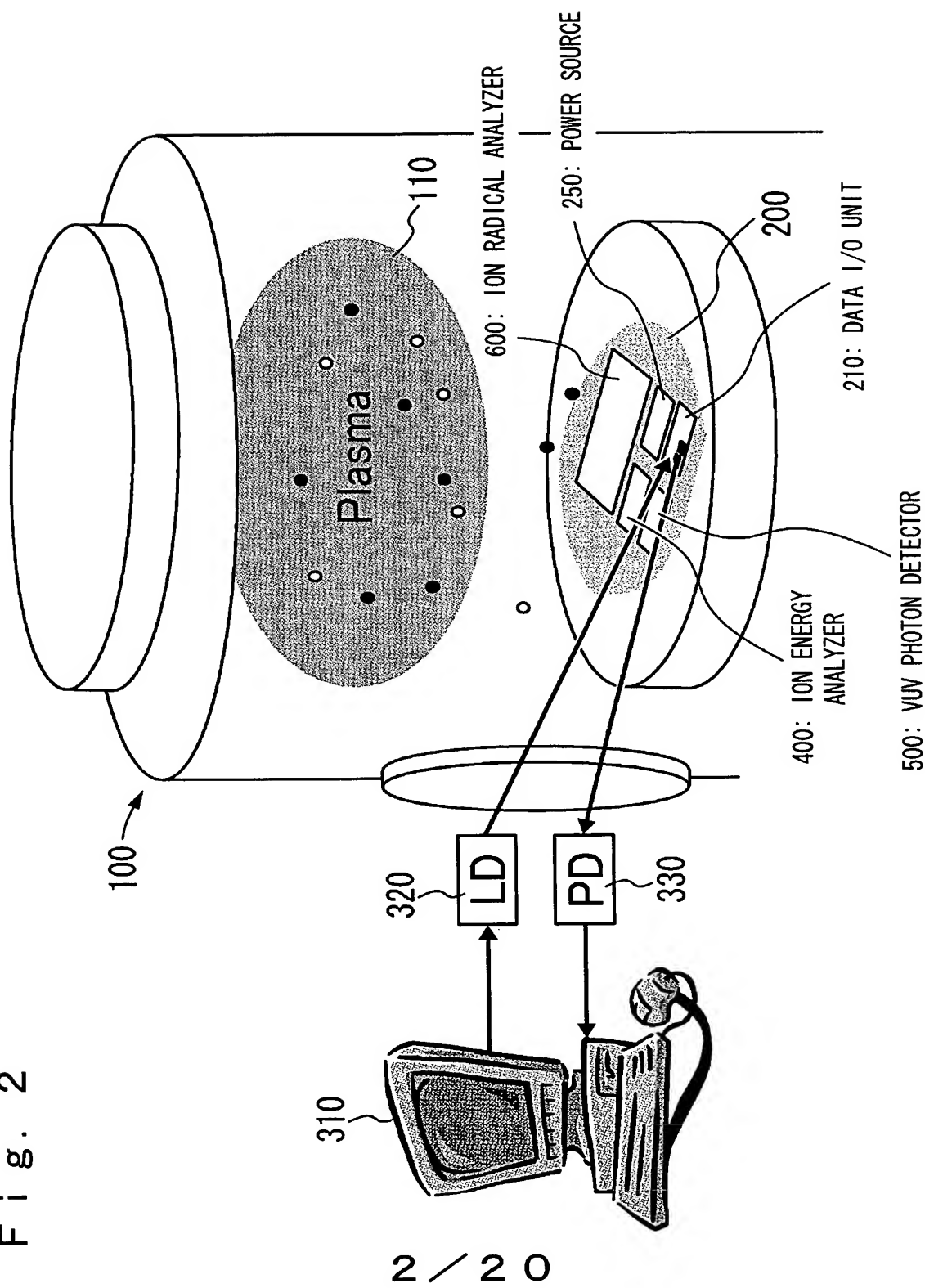


Fig. 2



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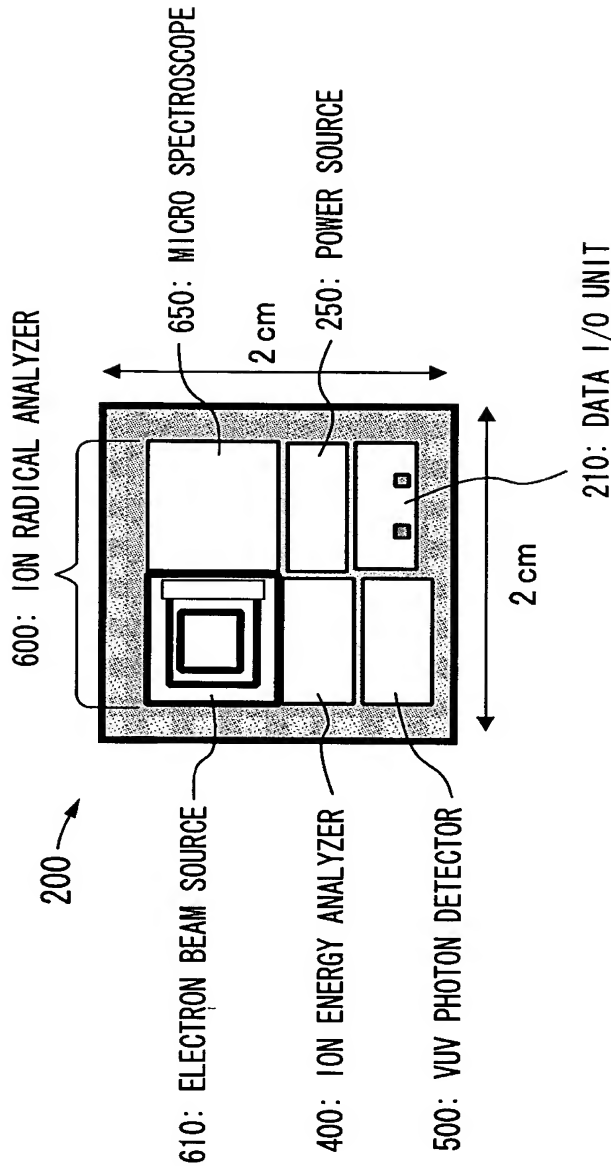


Fig. 4

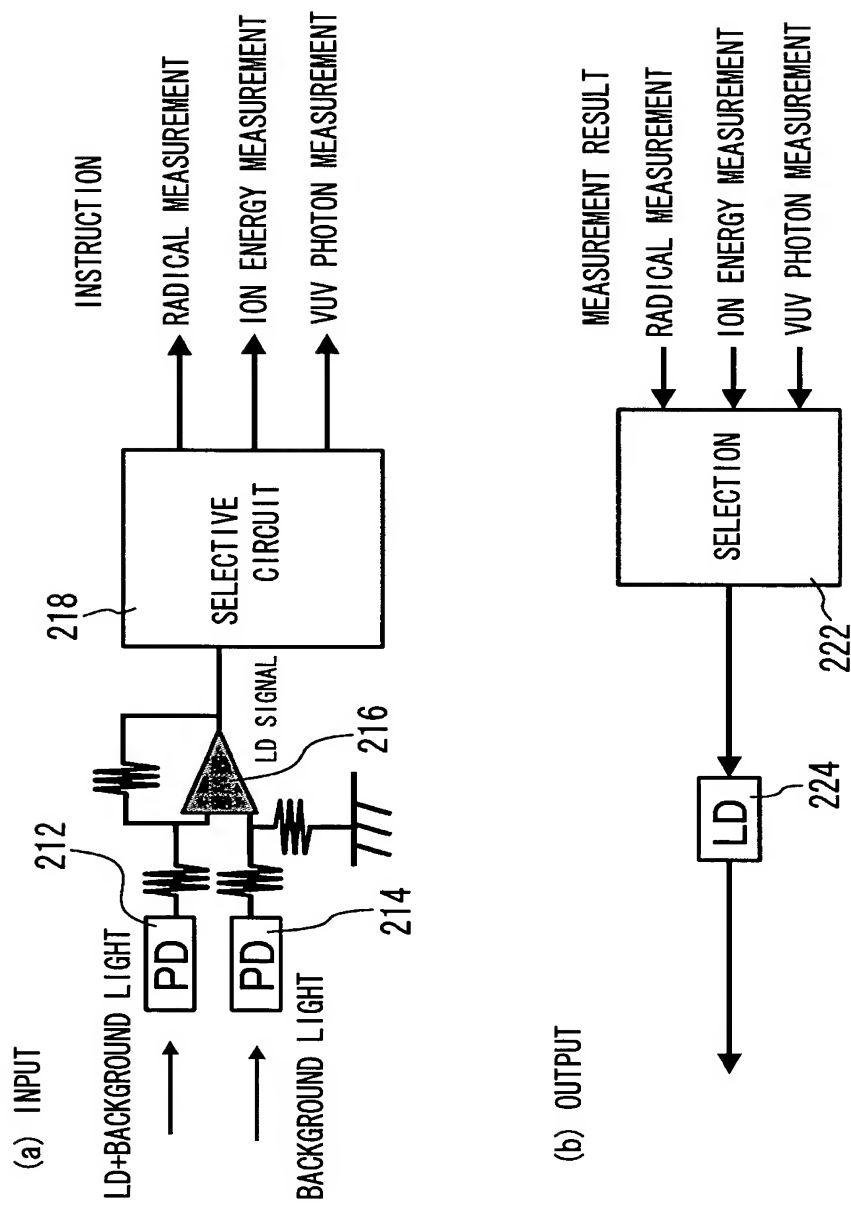


Fig. 5

POTENTIAL DIFFERENCE BETWEEN PLASMA SPACE POTENTIAL  
AND GROUND IS UTILIZED.

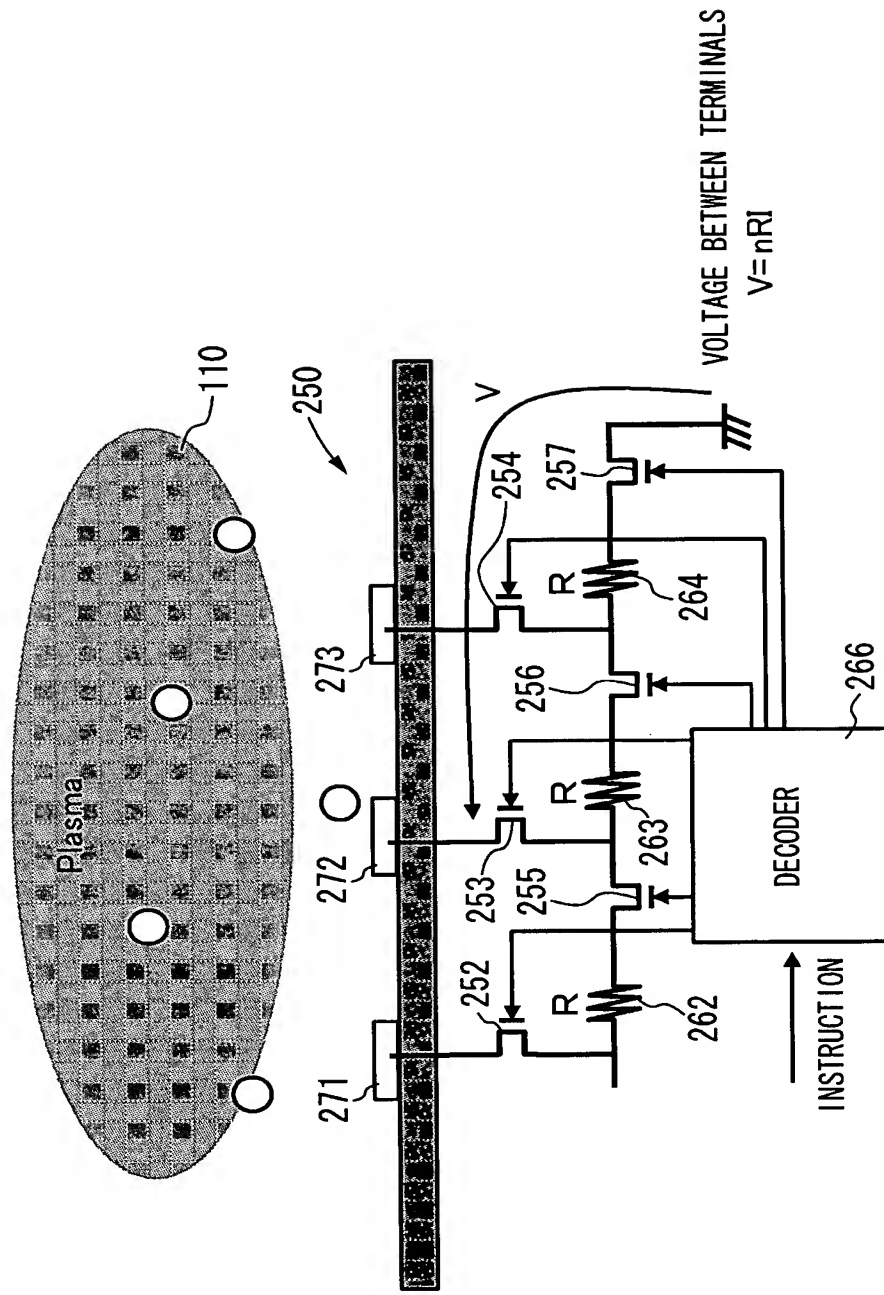


Fig. 6

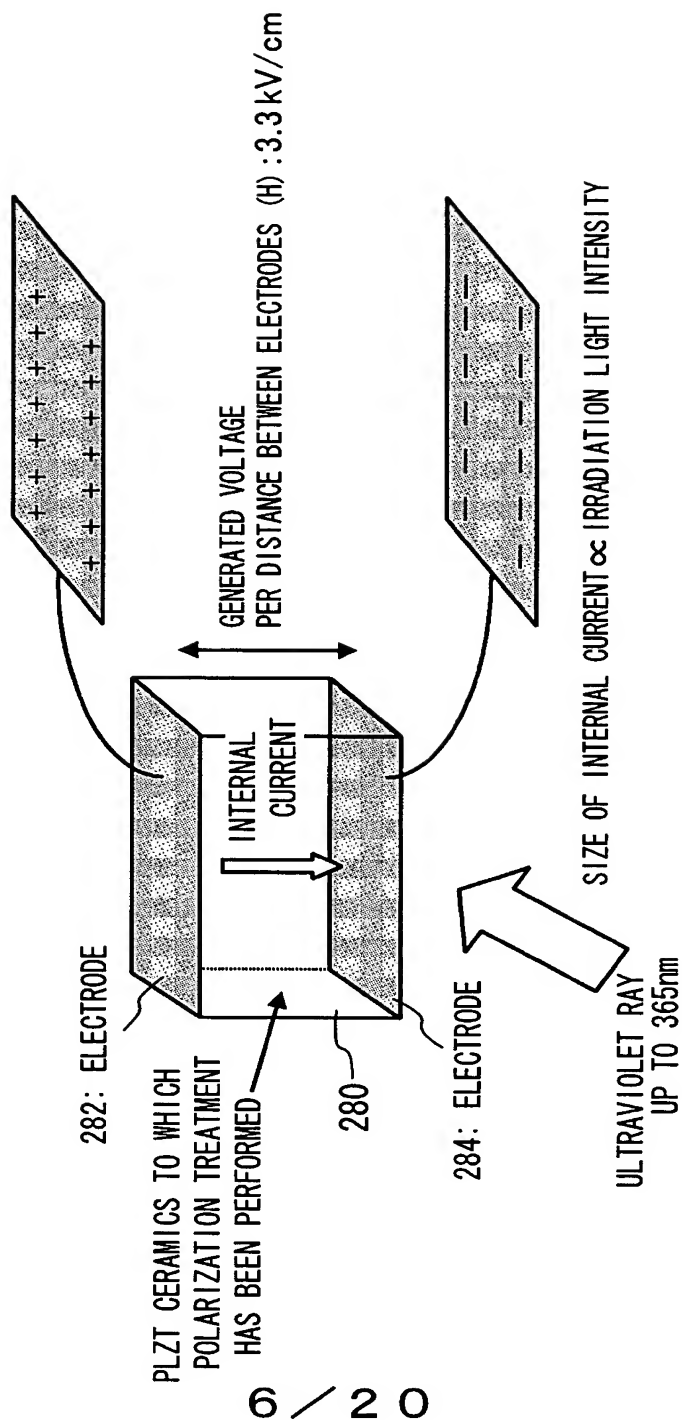


Fig. 7

STRUCTURE OF ON-WAFER ION ENERGY ANALYZER

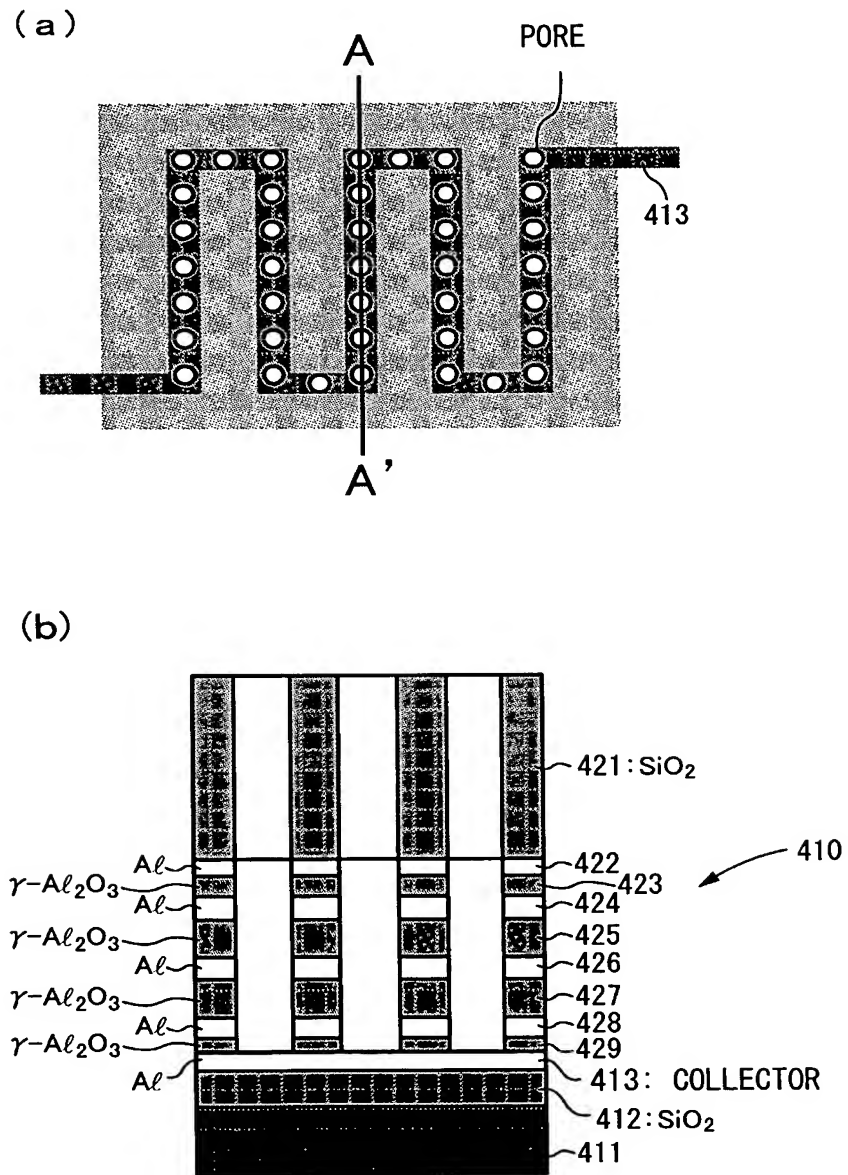




Fig. 8 MEASUREMENT SYSTEM OF ON-WAFER ION ENERGY ANALYZER

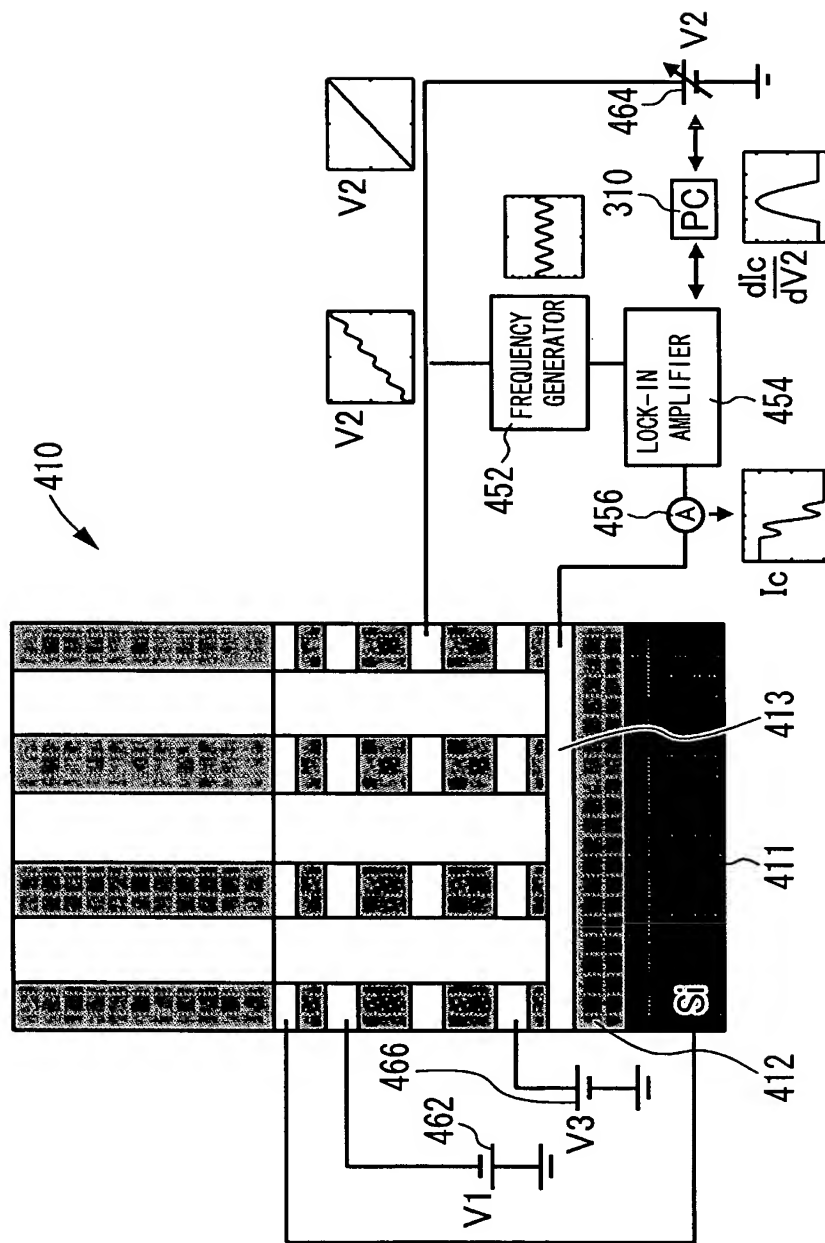


Fig. 9

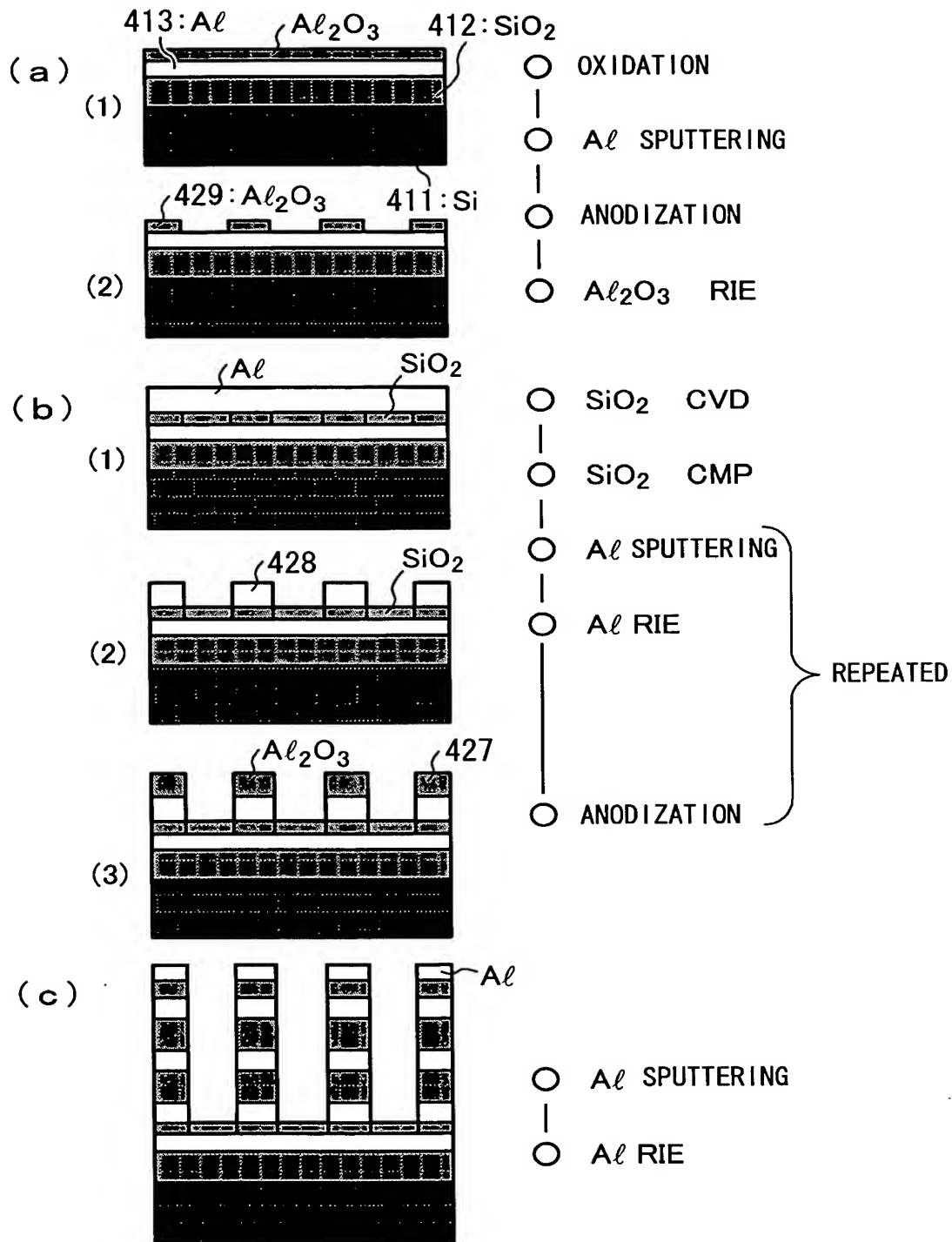


Fig. 10

(d)

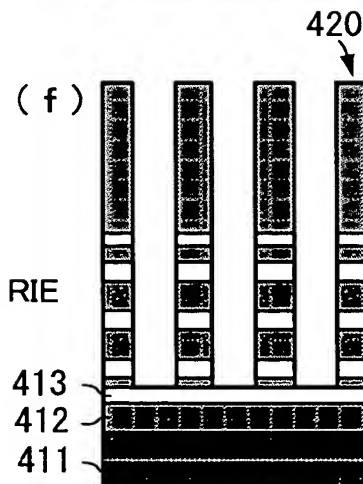
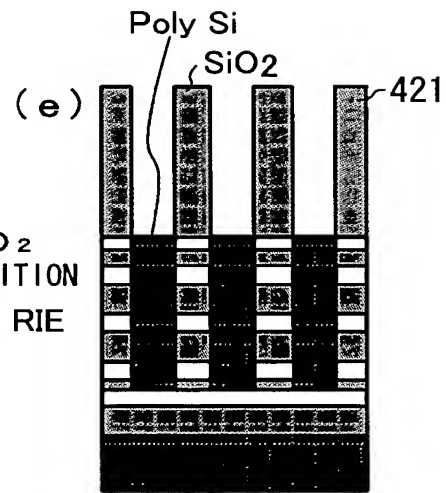
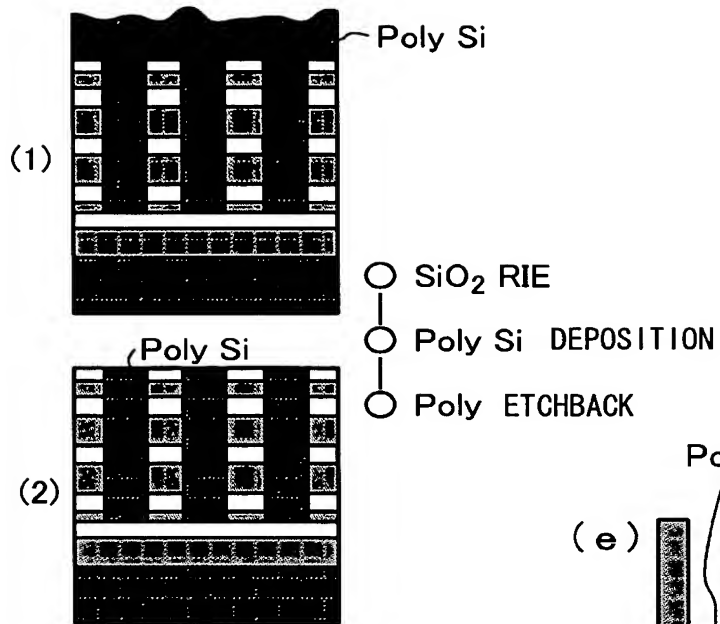


Fig. 11

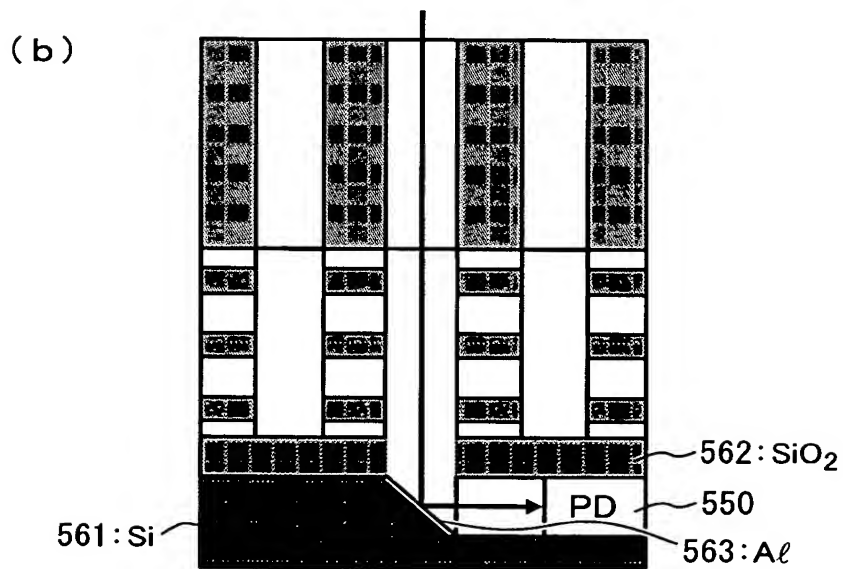
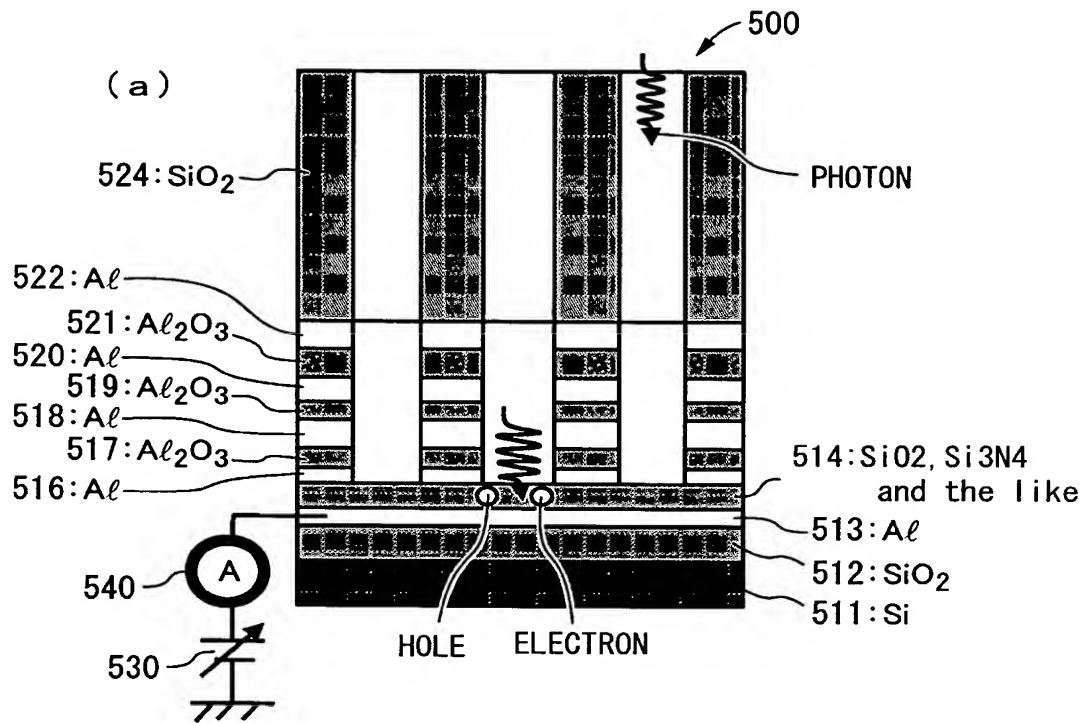


Fig. 12

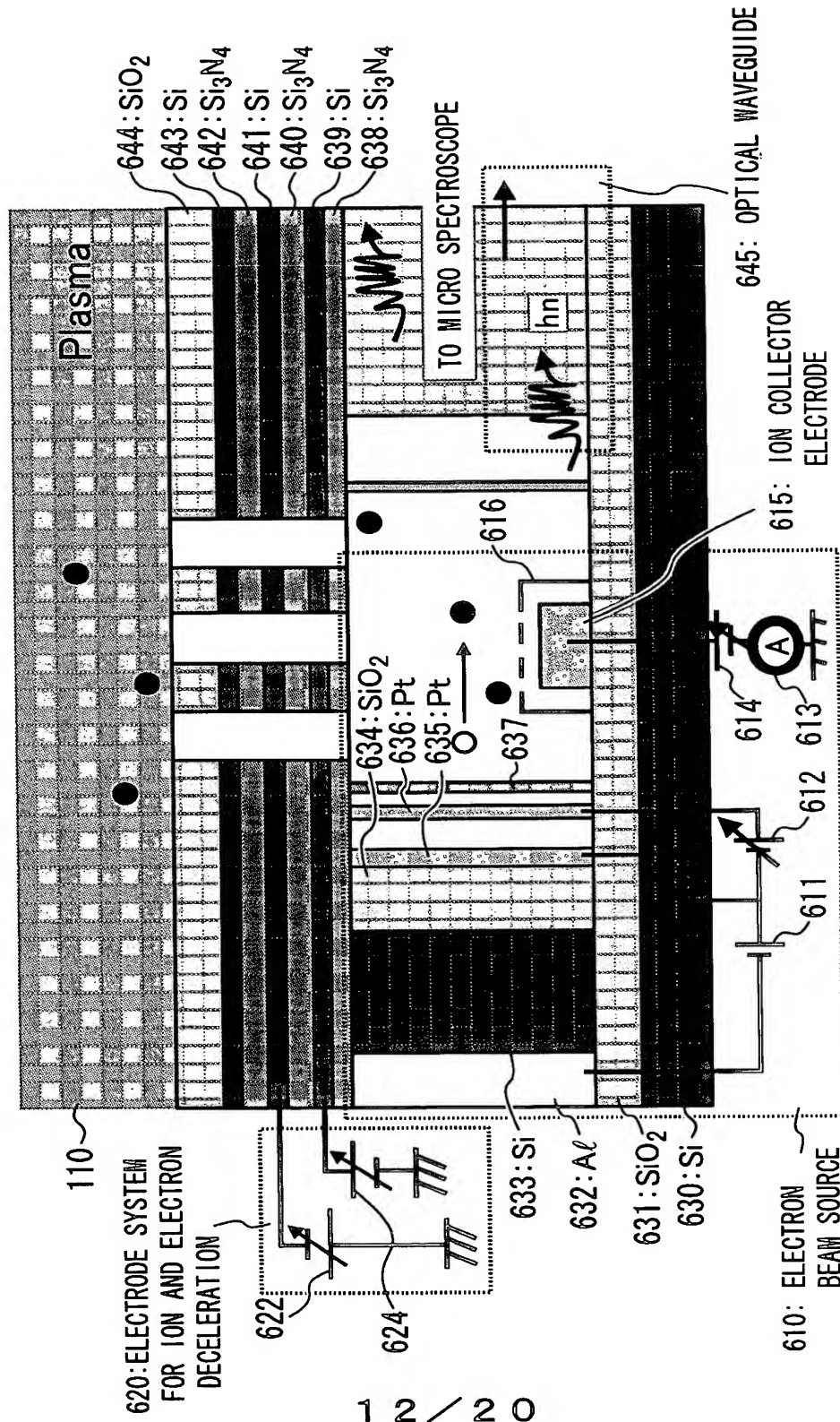


Fig. 13

# FIELD EMISSION TYPE ELECTRON GUN

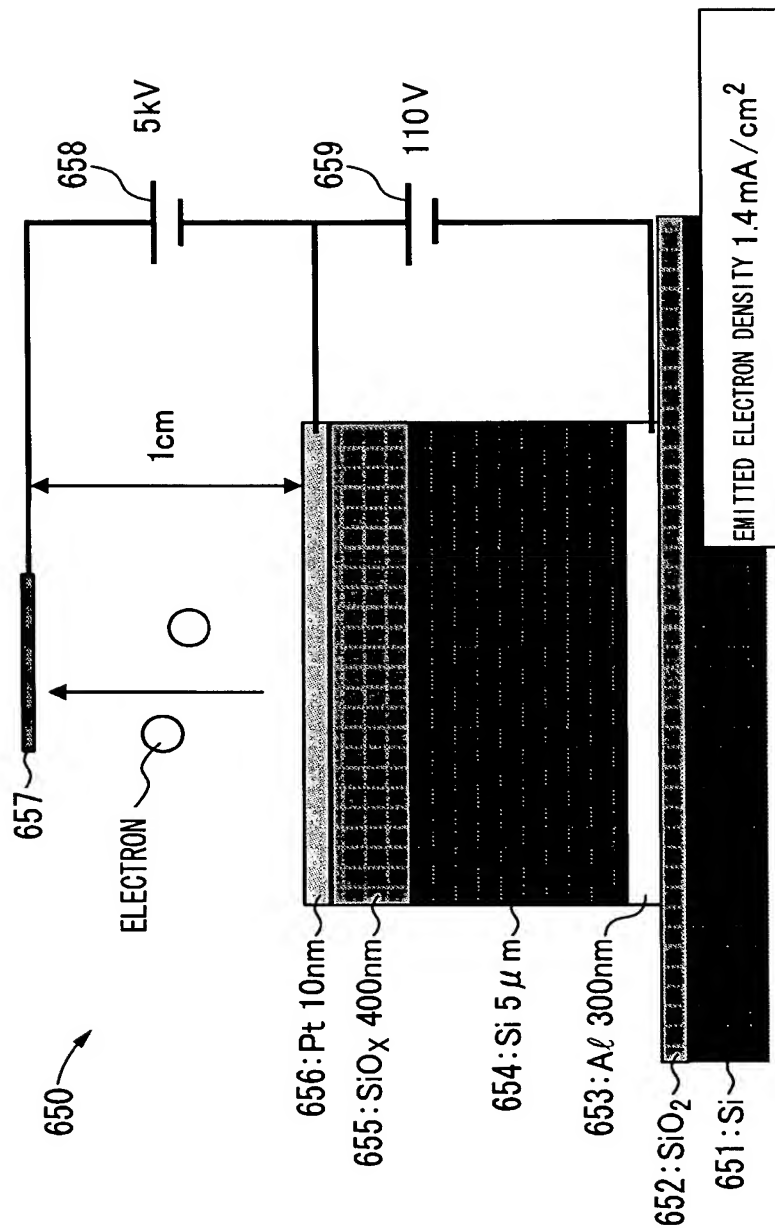


Fig. 14

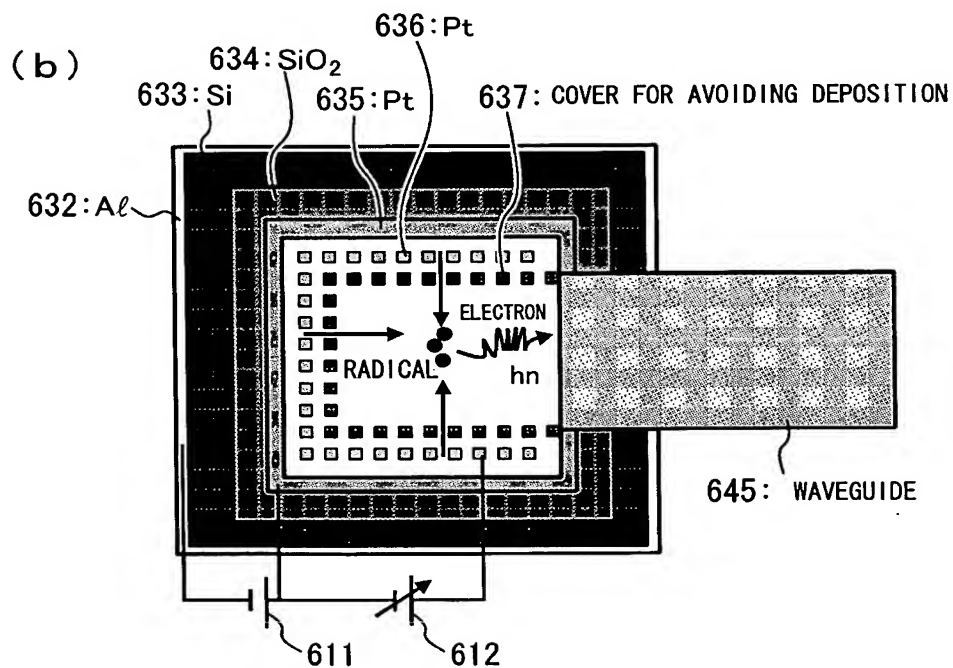
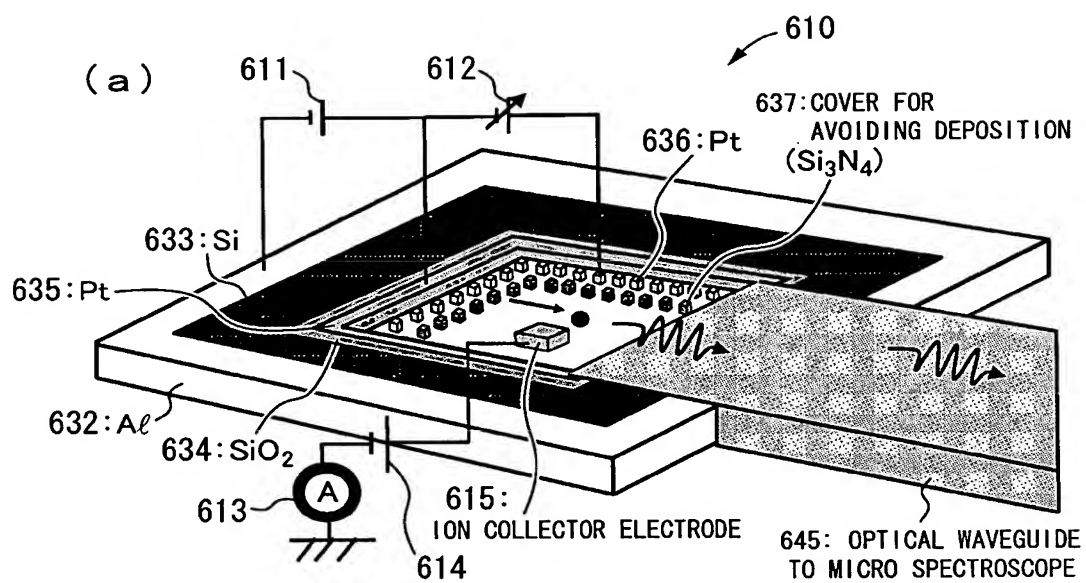


Fig. 15

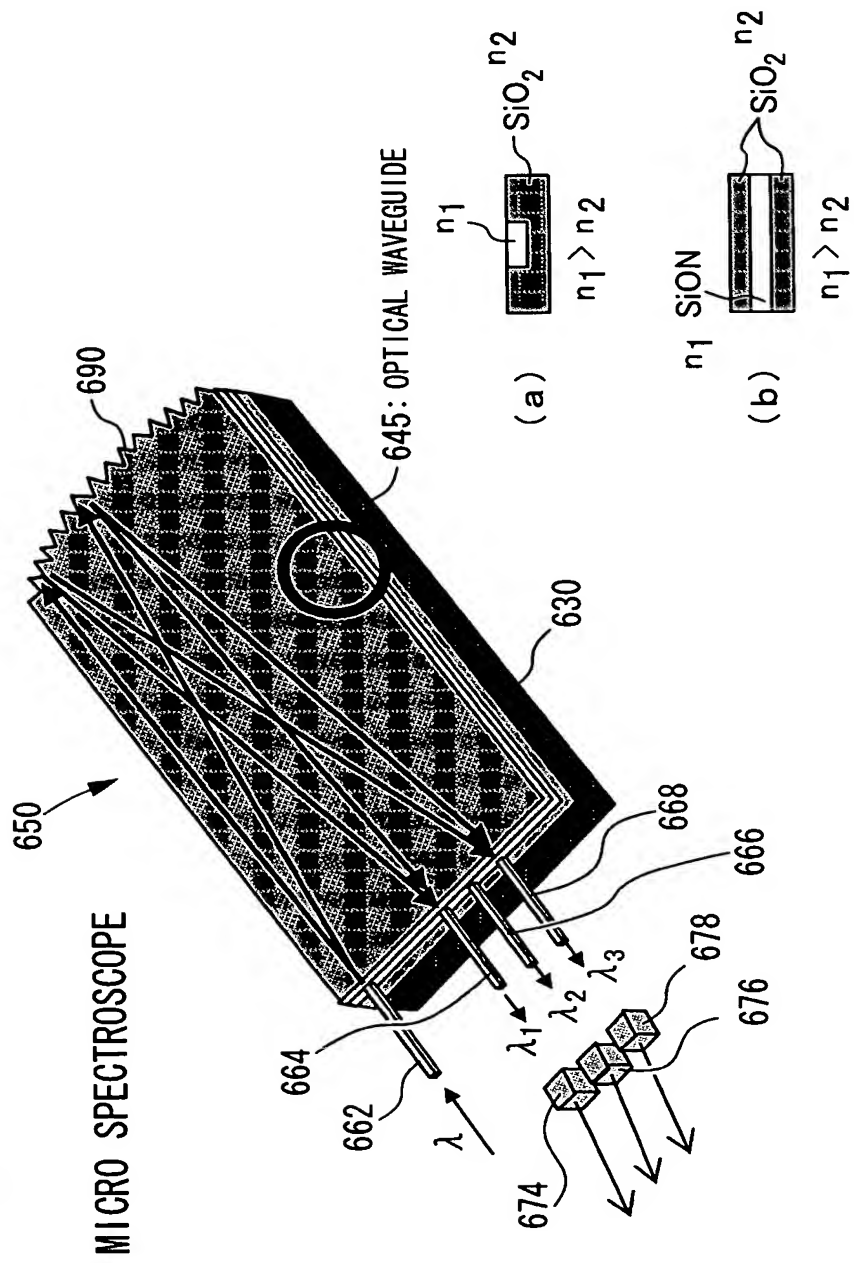






Fig. 16

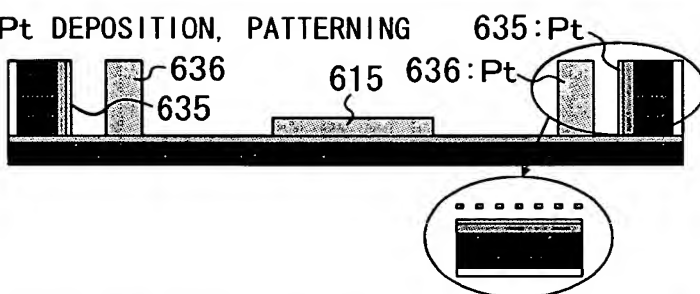
MANUFACTURING PROCESS OF MICRO ION RADICAL ANALYZER(1)

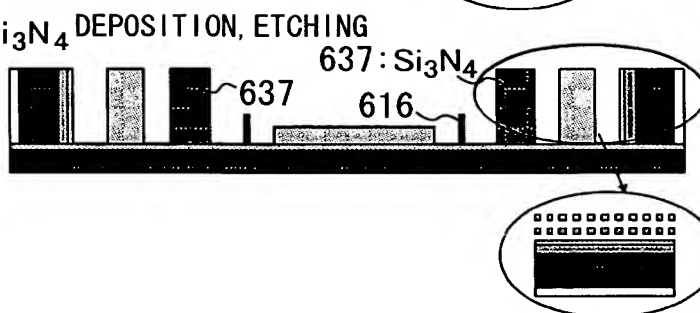
(a) OXIDATION  


(b) Si DEPOSITION, ETCHING  


(c) Al VAPOR DEPOSITION, ETCHING  


(d) SiO<sub>2</sub> DEPOSITION, ETCHING  


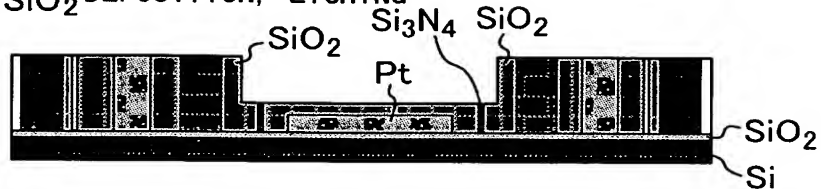
(e) Pt DEPOSITION, PATTERNING  


(f) Si<sub>3</sub>N<sub>4</sub> DEPOSITION, ETCHING  


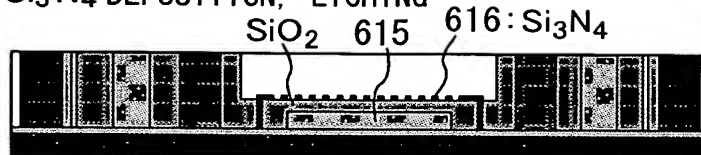
F i g . 1 7

MANUFACTURING PROCESS OF MICRO ION RADICAL ANALYZER(2)

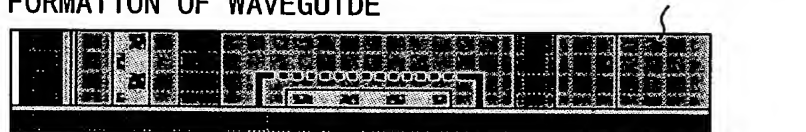
( g )  $\text{SiO}_2$  DEPOSITION, ETCHING



( h )  $\text{Si}_3\text{N}_4$  DEPOSITION, ETCHING



( i )  $\text{SiO}_2$  DEPOSITION, ETCHING  
FORMATION OF WAVEGUIDE



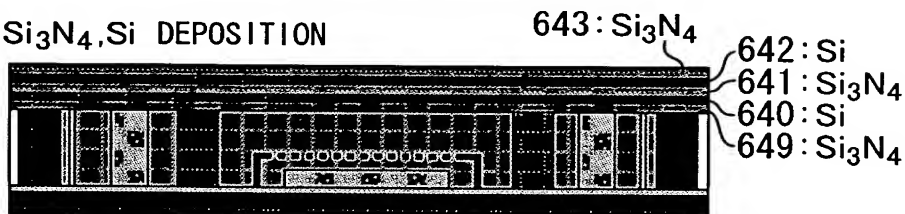
ELECTRON BEAM SOURCE



WAVEGUIDE

DOPING REGION  
 $n_1$   $n_2$   $n_2 > n_1$

( j )  $\text{Si}_3\text{N}_4$ , Si DEPOSITION



( k )  $\text{Si}_3\text{N}_4$ , Si ETCHING  
 $\text{SiO}_2$  SACRIFICE LAYER ETCHING

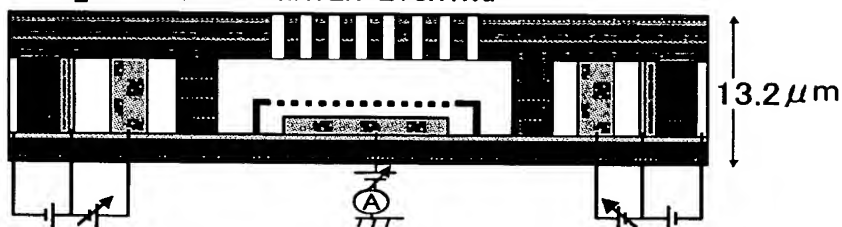


Fig. 18

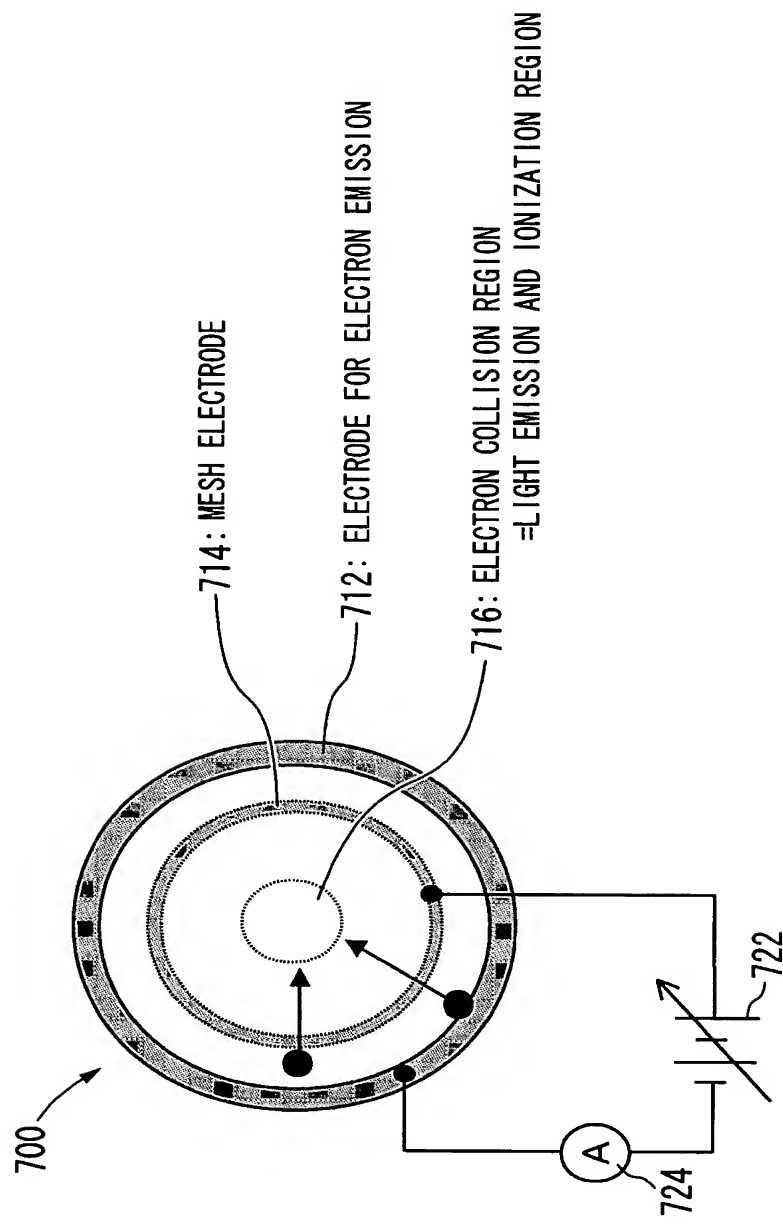
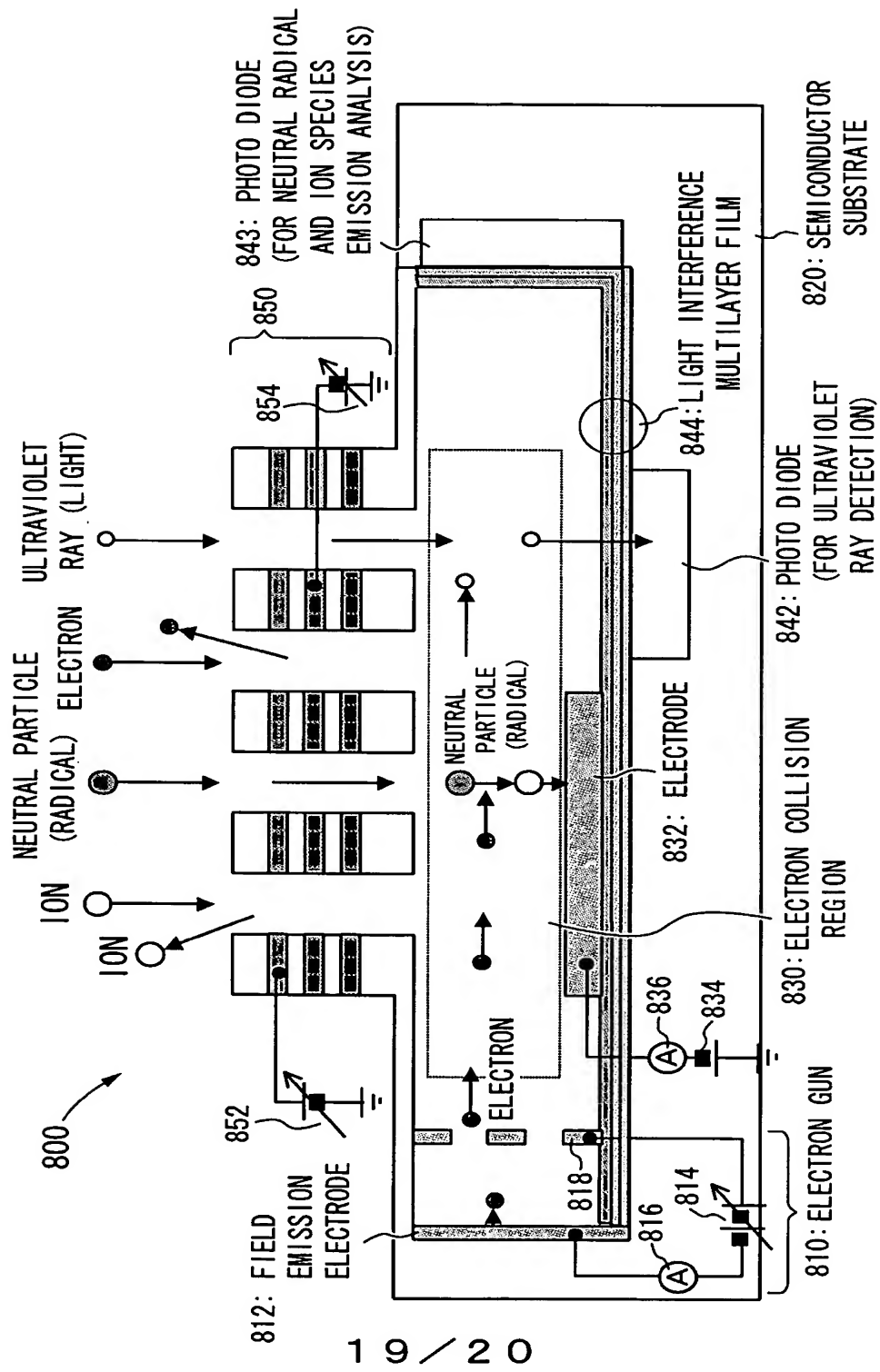
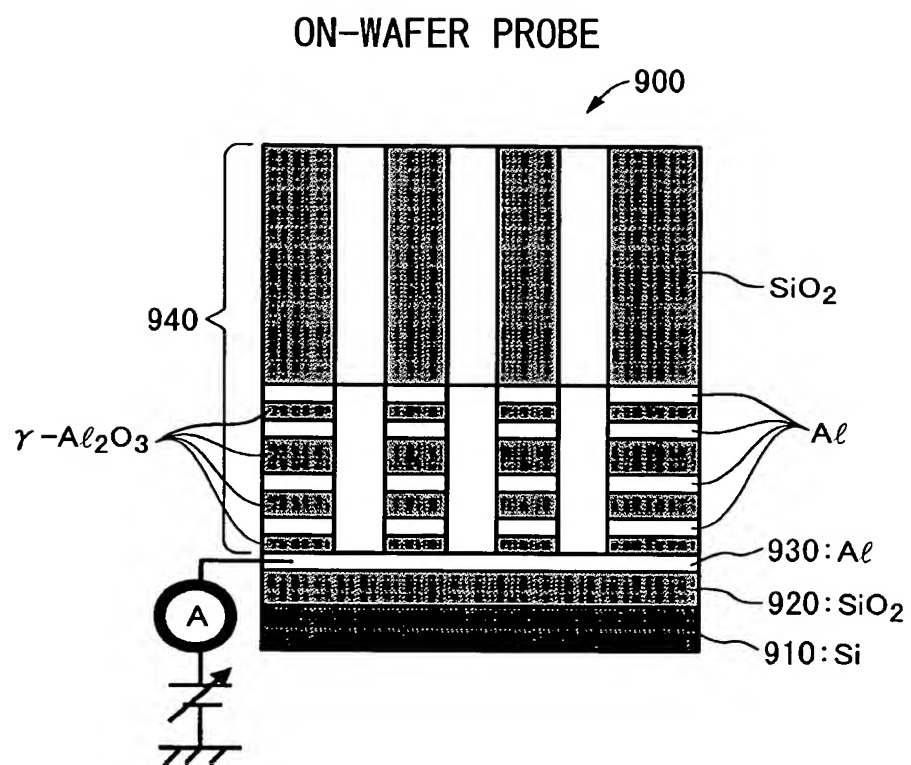


Fig. 19



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**【Title of Document】**

**Abstract**

**【Abstract】**

**【Problem】** The object of the present invention is to provide an on-wafer monitoring system capable of monitoring at the position of a substrate, which is an object to be processed, in the plasma treatment device.

**【A means for solving problem】** An on-wafer monitoring system (200) is placed at a position of a substrate to be treated in a plasma treatment device (100). The on-wafer monitoring system (200) includes various sensors, a data I/O unit (210) for optically inputting/outputting data to/from outside, and an internal power source unit (250) for supplying power to them. The on-wafer data I/O unit (210) is connected to a laser diode (LD) (320) and a photo diode (PD) (330) which are optical I/O units installed outside. The data I/O unit (210) receives an instruction from outside and transmits monitored data to outside. Sensors arranged on the substrate are an ion energy analyzer (400), a VUV photon detector (500), and a radical ion species emission spectrophotometer (600).